



**Cryptographic Hardware and Embedded Systems
- CHES 2000: Second International Workshop
Worcester, MA, USA, August 17-18, 2000
Proceedings (Lecture Notes in Computer Science)**

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These are the pre-proceedings of CHES 2000, the second workshop on Cryptographic Hardware and Embedded Systems. The first workshop, CHES'99, which was held at WPI in August 1999, was received quite enthusiastically by people in academia and industry who are interested in hardware and software implementations of cryptography. We believe there has been a long-standing need for a workshop series combining theory and practice for integrating strong data security into modern communications and e-commerce applications. We are very glad that we had the opportunity to serve this purpose and to create the CHES workshop series. As is evident by the papers in these proceedings, there have been many excellent contributions. Selecting the papers for this year's CHES was not an easy task, and we regret that we had to reject several good papers due to the limited availability of time. There were 51 submitted contributions to CHES 2000, of which 25 were selected for presentation. This corresponds to a paper acceptance rate of 49% for this year, which is a decrease from the 64% acceptance rate for CHES'99. All papers were reviewed. In addition to the contributed presentations, we have invited two speakers. As last year, the focus of the workshop is on all aspects of cryptographic hardware and embedded system design. Of special interest were contributions that describe new methods for efficient hardware implementations and high-speed software for embedded systems, e.g., smart cards, microprocessors, DSPs, etc. In addition, there were again several very interesting and innovative papers dealing with cryptanalysis in practice, ranging from side-channel attacks to FPGA-based attack hardware.

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